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E. J. J. J.
8-28-01
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Basil LUI

Application No.: 09/762,589

Docket No.: 108572

Filed: March 30, 2001

For: SEMICONDUCTOR DEVICE SIMULATION METHOD AND SIMULATOR

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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Date: June 15, 2001

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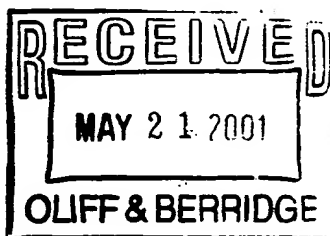
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/762,589	03/30/2001	2858	1230	108572	8	10 12	6

CONFIRMATION NO. 5262

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FILING RECEIPT



OC000000006068781

Date Mailed: 05/17/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Basil Lui, Cambridge, GBN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/GB00/02321 06/15/2000

Foreign Applications

UNITED KINGDOM 9913915.6 06/15/1999

Projected Publication Date: N/A

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Early Publication Request: No

Title

Semiconductor device simulation method and simulator

Preliminary Class

324

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Team : OIPE

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